

Short Summer Training for KMUTT student on 2009 October and November in UEC

Period: 4 weeks between 2009 October 18th and November 13th.

Location : UEC Department of Electronic Engg., 1-5-1,
Chofu, Tokyo 182-8585 Japan

UEC Supervisory staff :

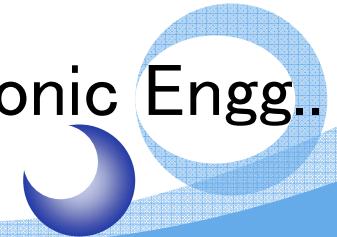
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KMUTT Leader :

Lecturer Kamon Jirasereeamornkul (Electronic Engg..
KMUTT)



Number of Training student:

One Trainee student Theeraphat Poomalee from
Dept. of Electronic .Engg was accepted to stay at
UEC for High Performance Digital Control
Experiments of DC-DC Converter training.

Program agenda

Advanced digital control of a DC-DC converter is performed, and it is verified by experiments that the start-up characteristics and the load sudden change characteristics are improvable. Therefore, the following procedures are required and the training is advanced according to following schedule.

- 10.18–10.25 Design and Manufacture of DC-DC Converter
- 10.26–10.31 Design and Manufacture of Digital Controller using DSP
- 11.1–11.7 Experiments of Start-up Responses
- 11.8–11.12 Experiments of Dynamic Load Responses
- 11.13 Presentation of Training Results

Training results

In this training program, student from KMUTT could manufactured DC-DC Converter

and the controller of DC-DC converter using DSP. He could experienced the experiments of high performance digital control of DC-DC converter. He studied the advanced digital control is very good compared to usual PI control from experiments.

Last day he showed the good results which he got by the presentation. This presentation was shown also in KMUTT through the teleconference.

To the following the data of a presentation and photographs of the situation of the presentation and training. are shown.

Presentation document

Robust DC-DC buck converter with fully digital controller

At The University of Electro-Communications (UEC), Japan

Presented by Theeraphat Poomalee
King Mongkut's University of Technology Thonburi

Outline

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Introduction

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Working principle

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Experimental results

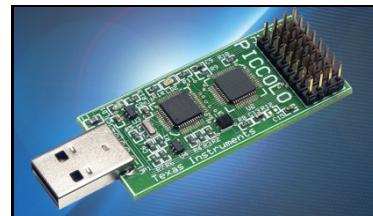
6

Conclusions

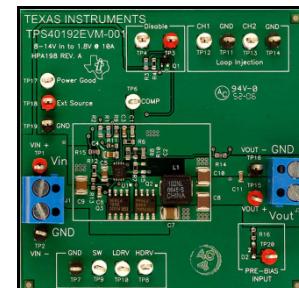
Introduction



- The demands on switching converter trend to increase its robustness capabilities.
- Conventional analog controller can be replaced by the digital controller to perform high flexible robust control system.
- Using DSP supports more complexity of control algorithm.

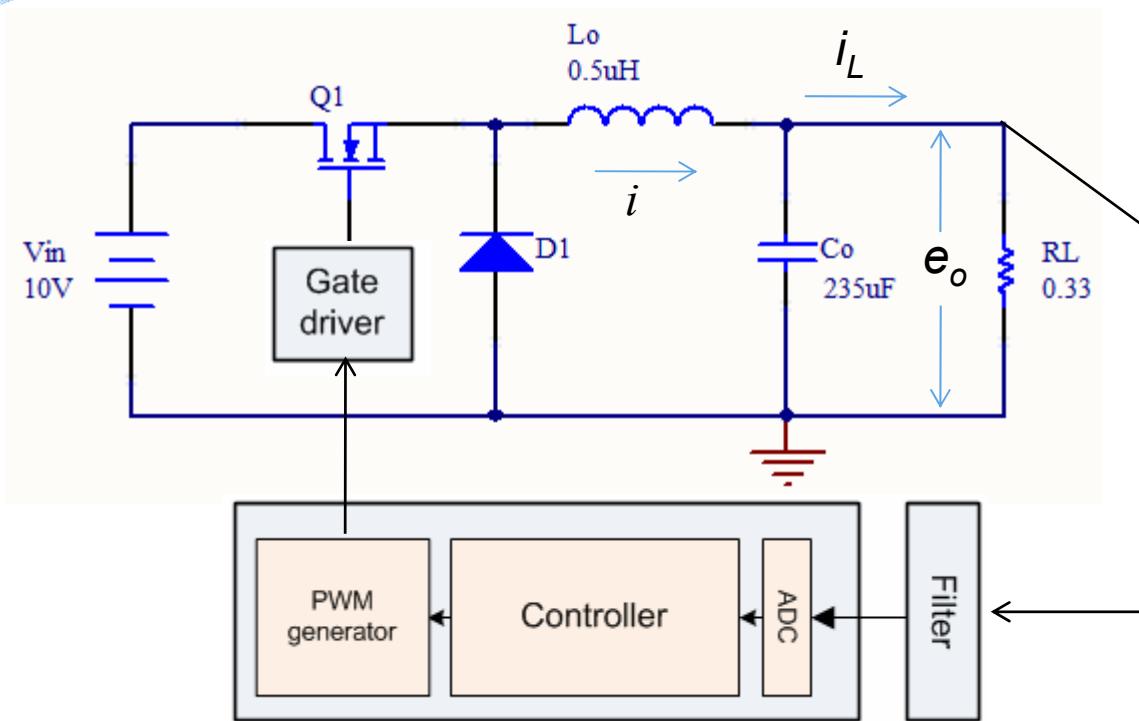


Digital signal processing unit



DC-DC Converter

Working principle of DC-DC converter



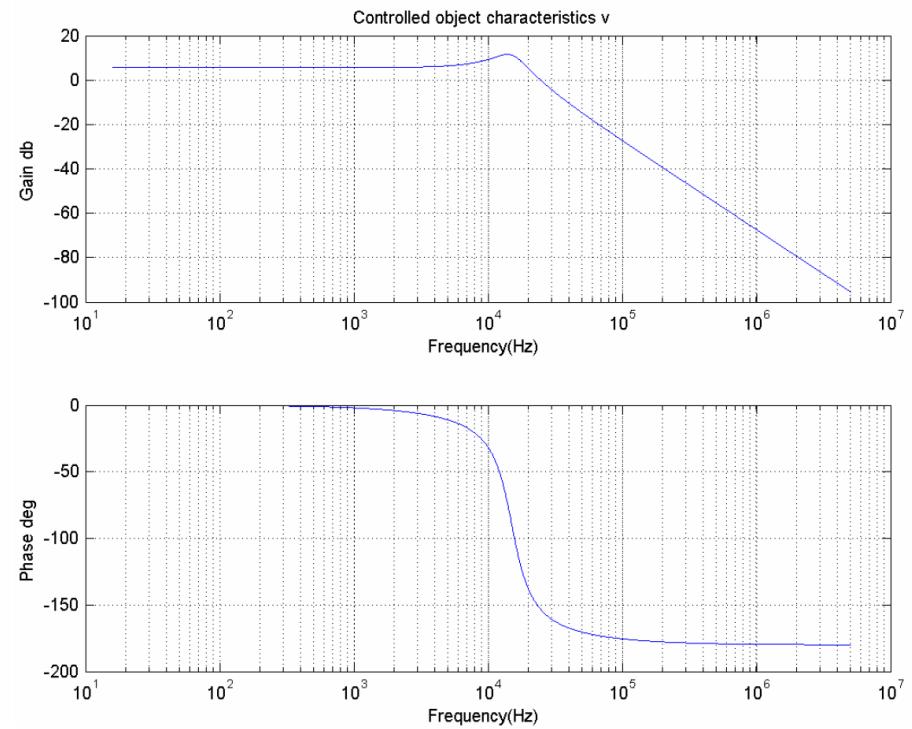
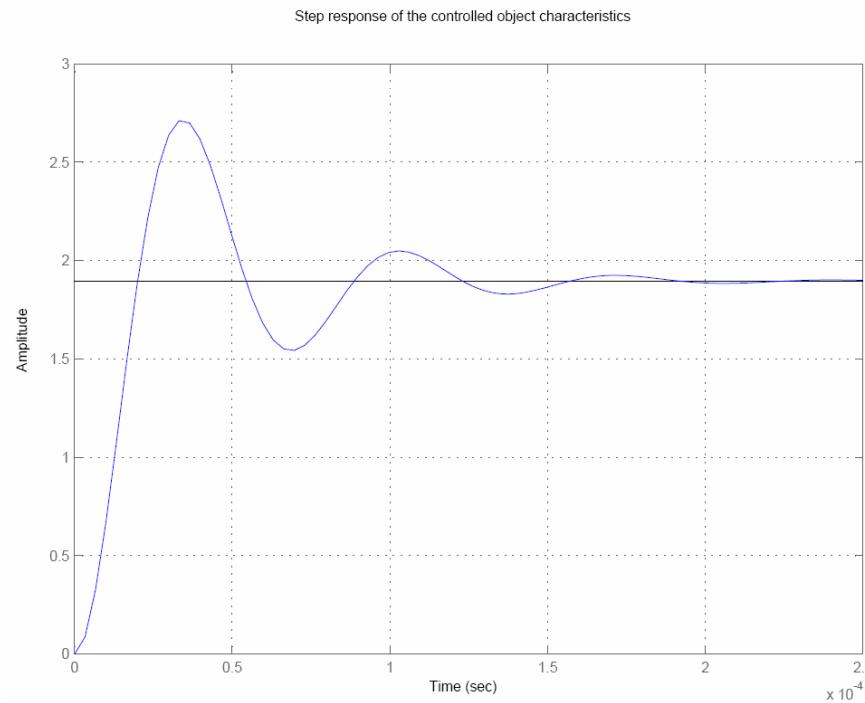
State space model

$$\begin{aligned} \dot{x} &= A_c x + B_c u & y &= Cx \\ x &= \begin{bmatrix} e_o \\ i \end{bmatrix} & y &= e_o & u &= e_i & G_p = 2 \\ A_c &= \begin{bmatrix} -\frac{1}{C_o R_L} & \frac{1}{C_o} \\ -\frac{1}{L_o} & -\frac{R_o}{L_o} \end{bmatrix} & B_c &= \begin{bmatrix} 0 \\ \frac{G_p}{L_o} \end{bmatrix} \\ C &= [1 \quad 0] \end{aligned}$$

R_o is combined resistance of the switch Q1 and the inductor L_o.

Working principle

Simulation results of the controlled object



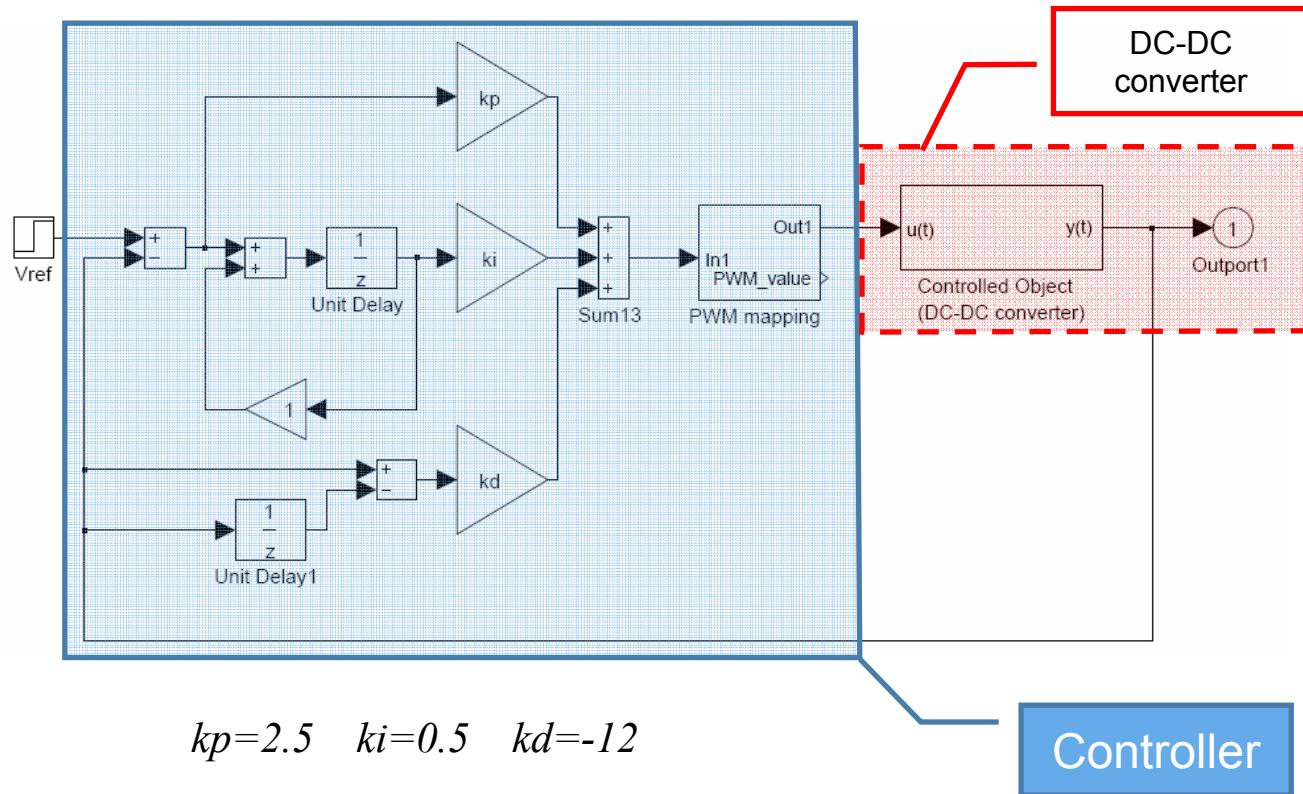
Open-loop characteristic of controlled object

- $RL = 0.33 \text{ Ohms}$
- $Lo = 0.5 \mu\text{H}$
- $Co = 235 \mu\text{H}$

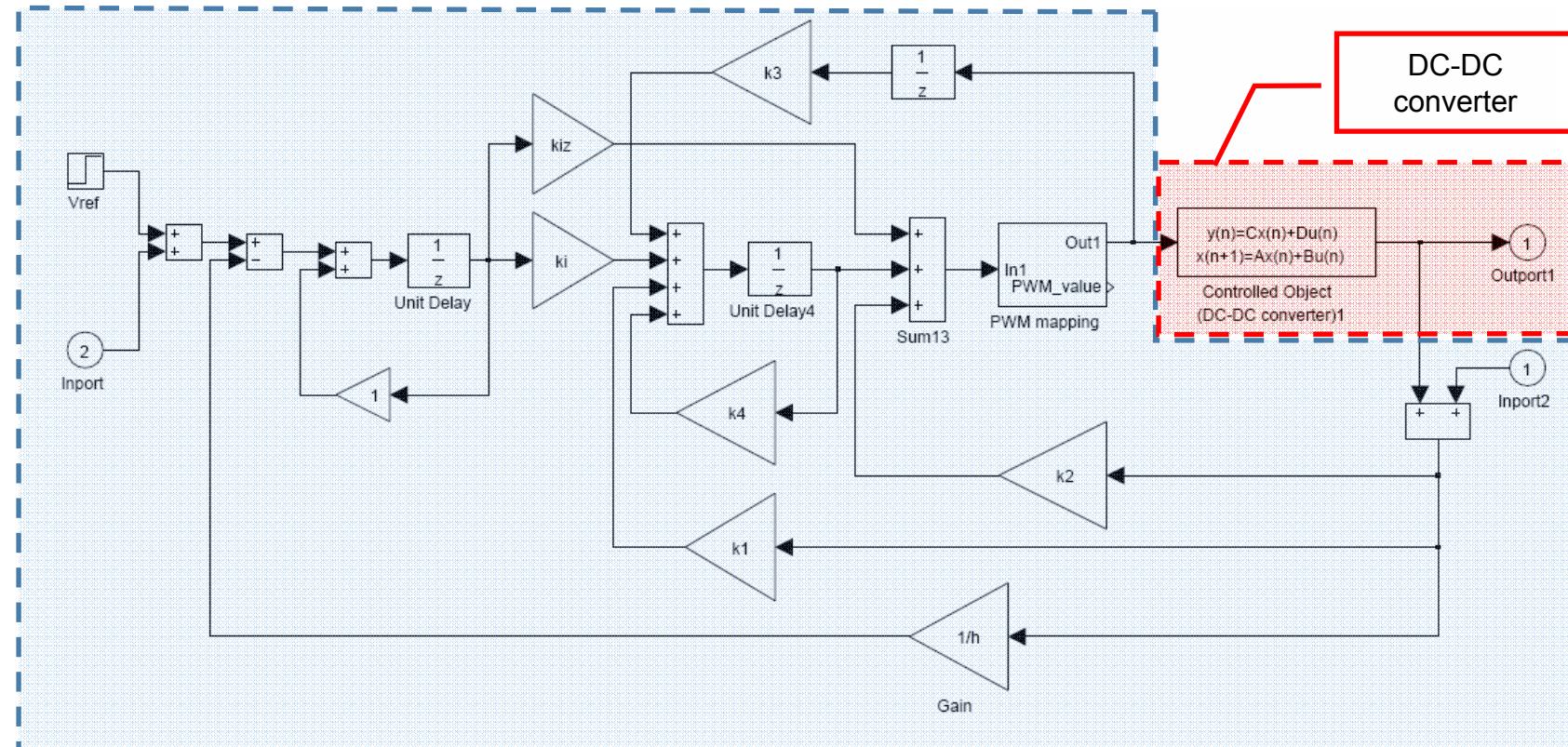
System poles $\rightarrow -2.44 \times 10^4 \pm 9.15 \times 10^4 i$
Overshoot = 44%

Closed-loop PID control

Applying simple PID controller



Closed-loop robust control



2DOF digital controller with estimated state feedback

Controller

Closed-loop robust control

Applying 2 degree-of-freedom digital controller using state feedback

$$k1 = (-F(1,1) - F(1,2)*FF(1,1) + ((-F(1,4) - F(1,2)*FF(1,4))*(-F(1,2)/FF(1,2))) - (G*H*H4 + G*H*Fz)*(Kz)*(1/(1+H2)))$$

$$k2 = (-F(1,2)/FF(1,2) - G*H*(Kz)*(1/(1+H2)))$$

$$k3 = (-F(1,3) - F(1,2)*(FF(1,3)))$$

$$k4 = (Fz)$$

$$ki = (h*(G*H*H4 + G*H*Fz)*(Kz)*(1))$$

$$kiz = (h*G*H*Kz)$$

where

$$FF(1,4) = -Bd(1,1)/Ad(1,2)$$

$$FF(1,2) = Ad(1,2)$$

$$FF(1,1) = -Ad(1,1)/Ad(1,2)$$

$$FF(1,3) = -Ad(1,3)/Ad(1,2)$$

$$Fz = -F(1,4) - F(1,2)*FF(1,4)$$

$$Ad = \begin{bmatrix} e^{AdT} & e^{Ad(T-Ld)} & \int_0^{Ld} e^{Ad\tau} B_o d\tau \\ 0 & 0 & 0 \end{bmatrix}$$

$$Bd = \begin{bmatrix} \int_0^{T-Ld} e^{Ad\tau} B_o d\tau \\ 1 \end{bmatrix}$$

$$C_d = [C \quad 0]$$

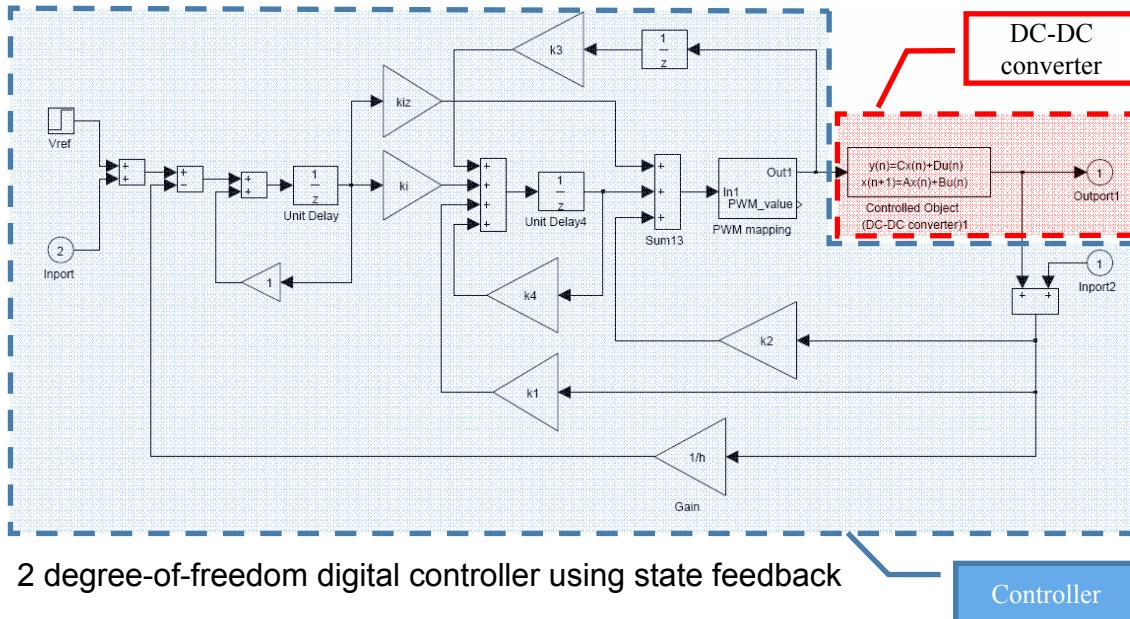
And

Ad , Bd are expanded discrete time state space model.

F is state feed-back matrices.

GH is steady state gain.

Experiment



2 degree-of-freedom digital controller using state feedback

Controller

- ❖ Input voltage = 9-11 V
- ❖ Output voltage = 3.3 V
- ❖ Output ripple at full load < 100 mV_{rms}

Controller properties

- ❖ PWM frequency 200 kHz
- ❖ PWM step resolution 150

$$\begin{aligned} H1 &= -0.2 \\ H2 &= -0.9 \\ H3 &= -0.1 \\ H4 &= -0.5 \\ Kz &= 0.2 \end{aligned}$$

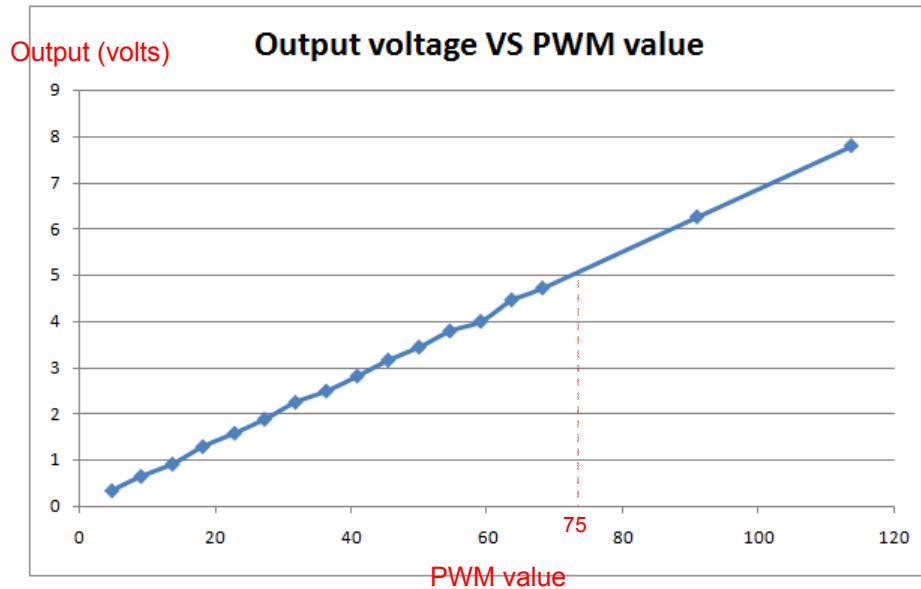
Desired feedback system
poles & zeroes

$$\begin{aligned} k1 &= 0.3402 \\ k2 &= 0.0210 \\ k3 &= -0.0058 \\ k4 &= 0.1122 \\ ki &= -0.0301 \\ kiz &= 0.0727 \end{aligned}$$

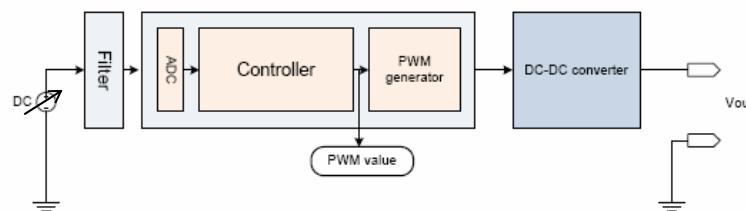
Parameters of controller

Experiment

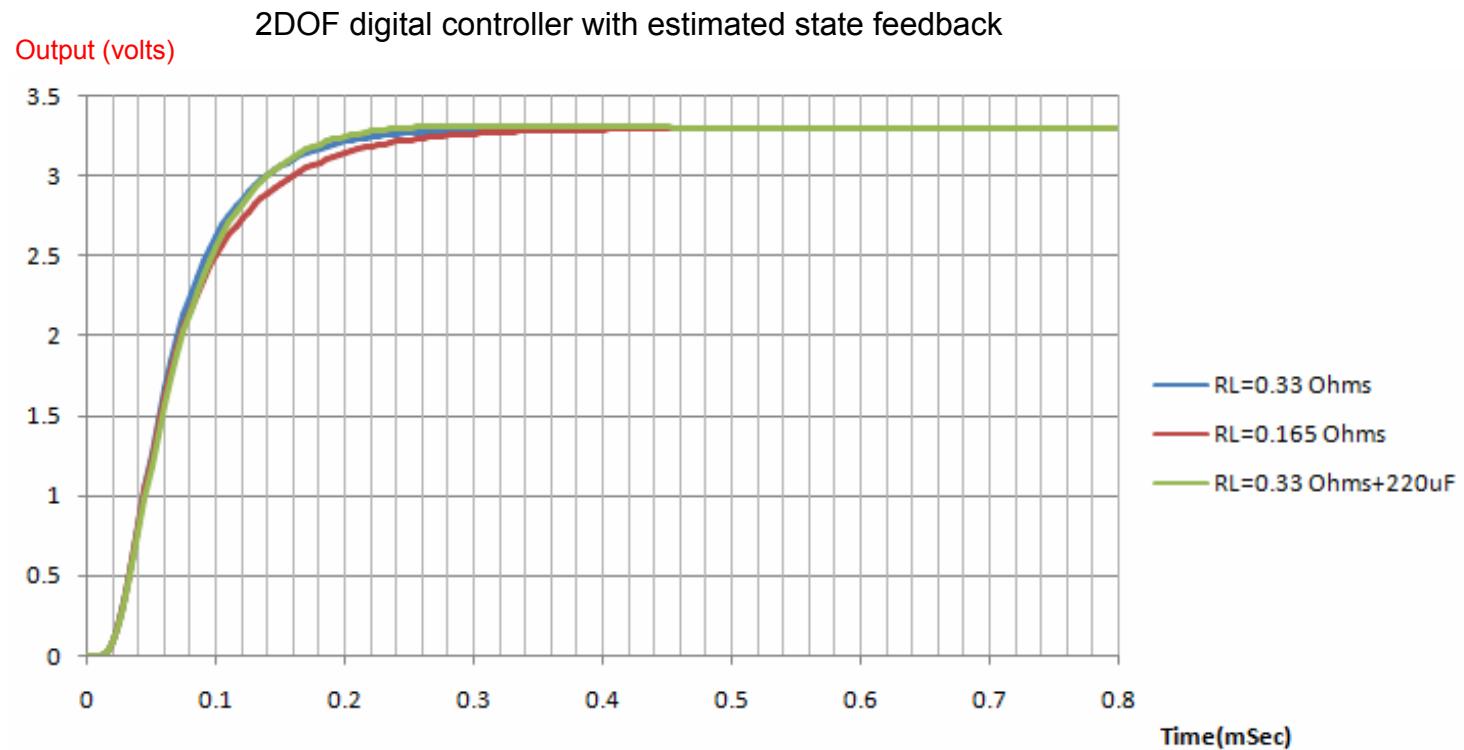
Open loop gain of the plant testing



- Switching frequency = 200 kHz
- Sampling frequency = 200 kHz
- PWM value 0-150 is mapped into duty cycle 0-100
- Input voltage of converter = 10 Volts



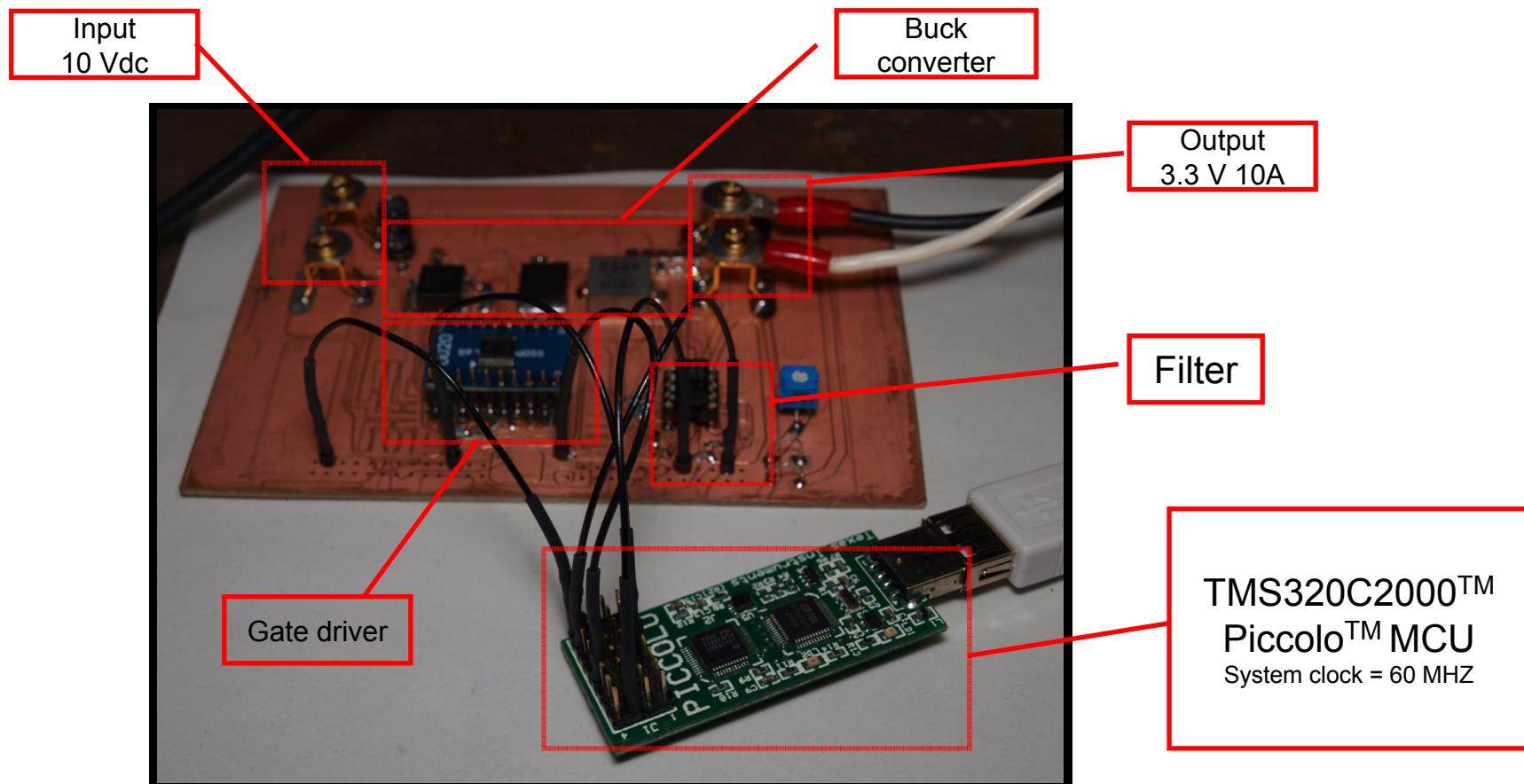
Simulation results



Step response in different load

Experimental results

Hardware implementation



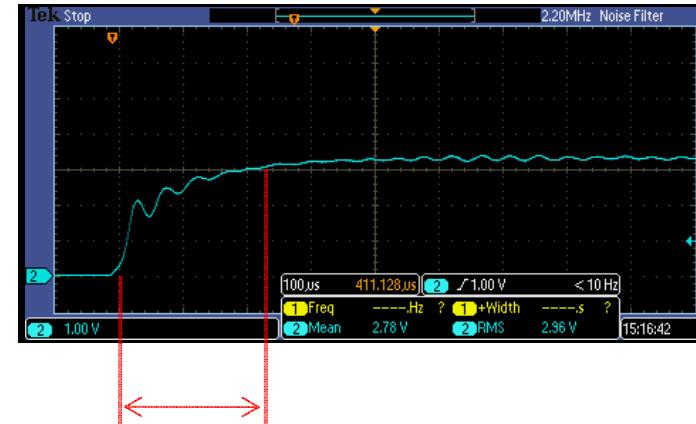
Experimental results

Start-up response

2 DOF digital controller



PID controller



$$R_L = 0.33\text{ Ohms} \quad \text{Time/DIV} = 100\text{uS}$$

Experimental results

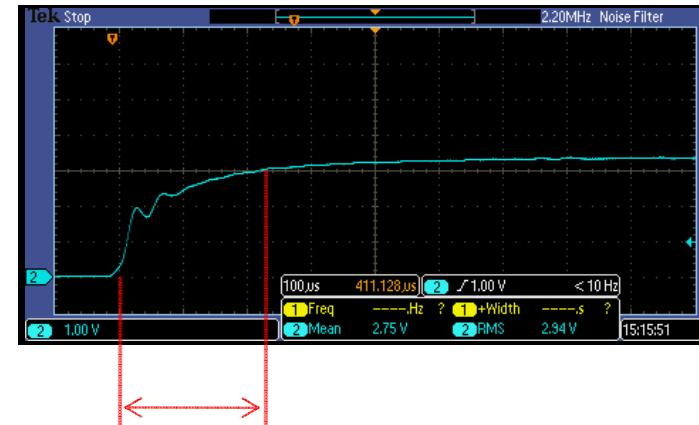
Start-up response

2 DOF digital controller



Start-up time = 140 uS

PID controller



Start-up time = 225 uS

$$R_L = 0.25 \text{ Ohms} \quad \text{Time/DIV} = 100\mu\text{s}$$

Experimental results

Start-up response

2 DOF digital controller



Start-up time = 140 uS

PID controller

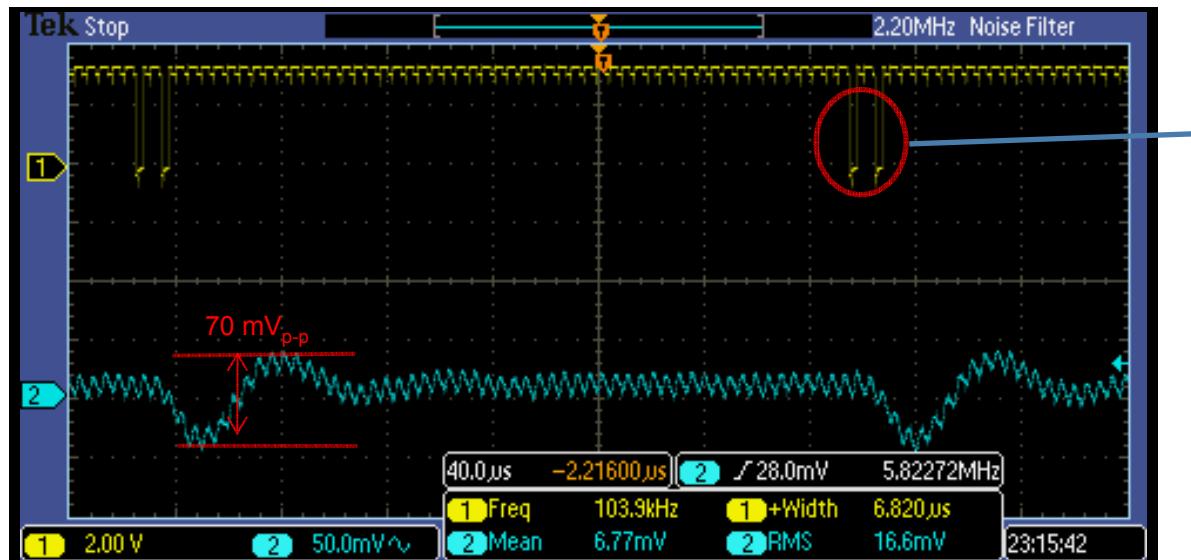


Start-up time = 225 uS

$$R_L = 0.33 + 220\mu F \text{ Ohms} \quad \text{Time/DIV} = 100\mu S$$

Experimental results

Steady state



Event indicator
shows the alteration
of the duty cycle

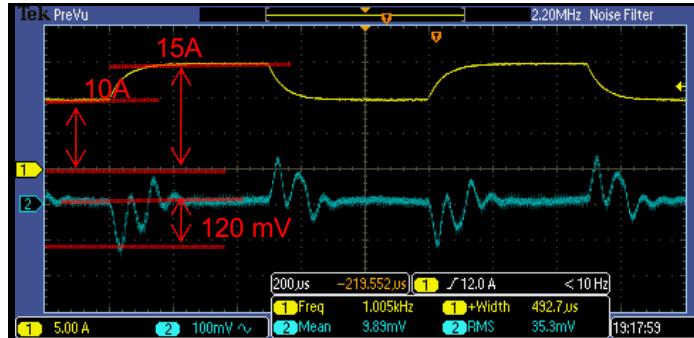
$$R_L = 0.33 \text{ Ohms}$$

Output ripple = 70 mV_{p-p}
Time/DIV = 100μS

Experimental results

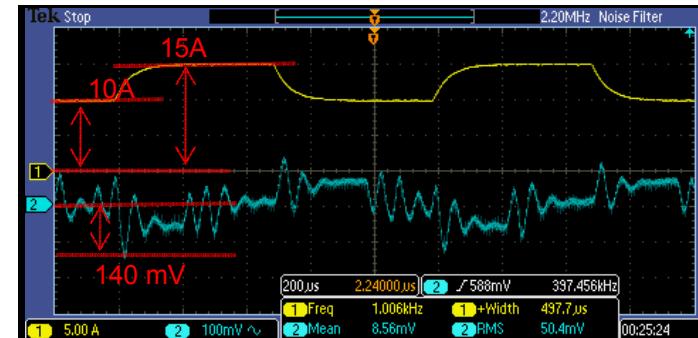
Dynamic load response

2 DOF digital controller



Output voltage variation = 120mV
Time/DIV = 200 μ S

PID controller



Output voltage variation = 140mV
Time/DIV = 200 μ S

Experimental results

2 DOF digital controller

- ❖ Provides fast start-up and good dynamic-load response.
- ❖ No overshoot and ringing appeared in start-up phase within various load.

Simple PID controller

- ❖ Some ringing and overshoot may be arisen in start-up period.
- ❖ Slower response to the dynamic load.
- ❖ Fast controller and higher loop gain may produce some oscillation.

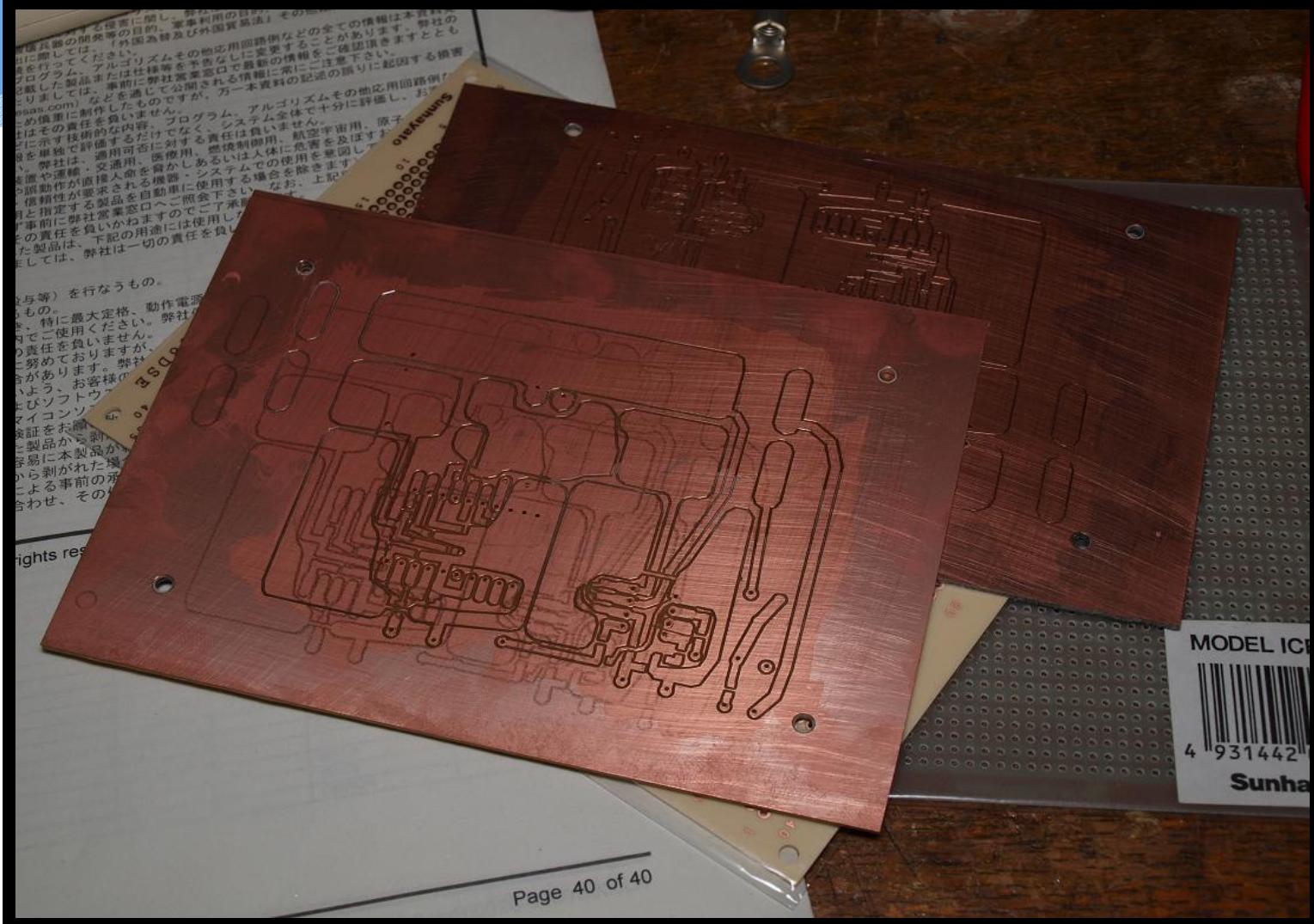
Conclusions

- ❖ The robust control algorithm can be achieved via the use of digital controller .
- ❖ The implemented system can overcome the variation of dynamic load as well as good start-up response.
- ❖ Only output voltage feedback is used.
- ❖ The proposed algorithm requires high performance DSP and high resolution of PWM generator.

Reference

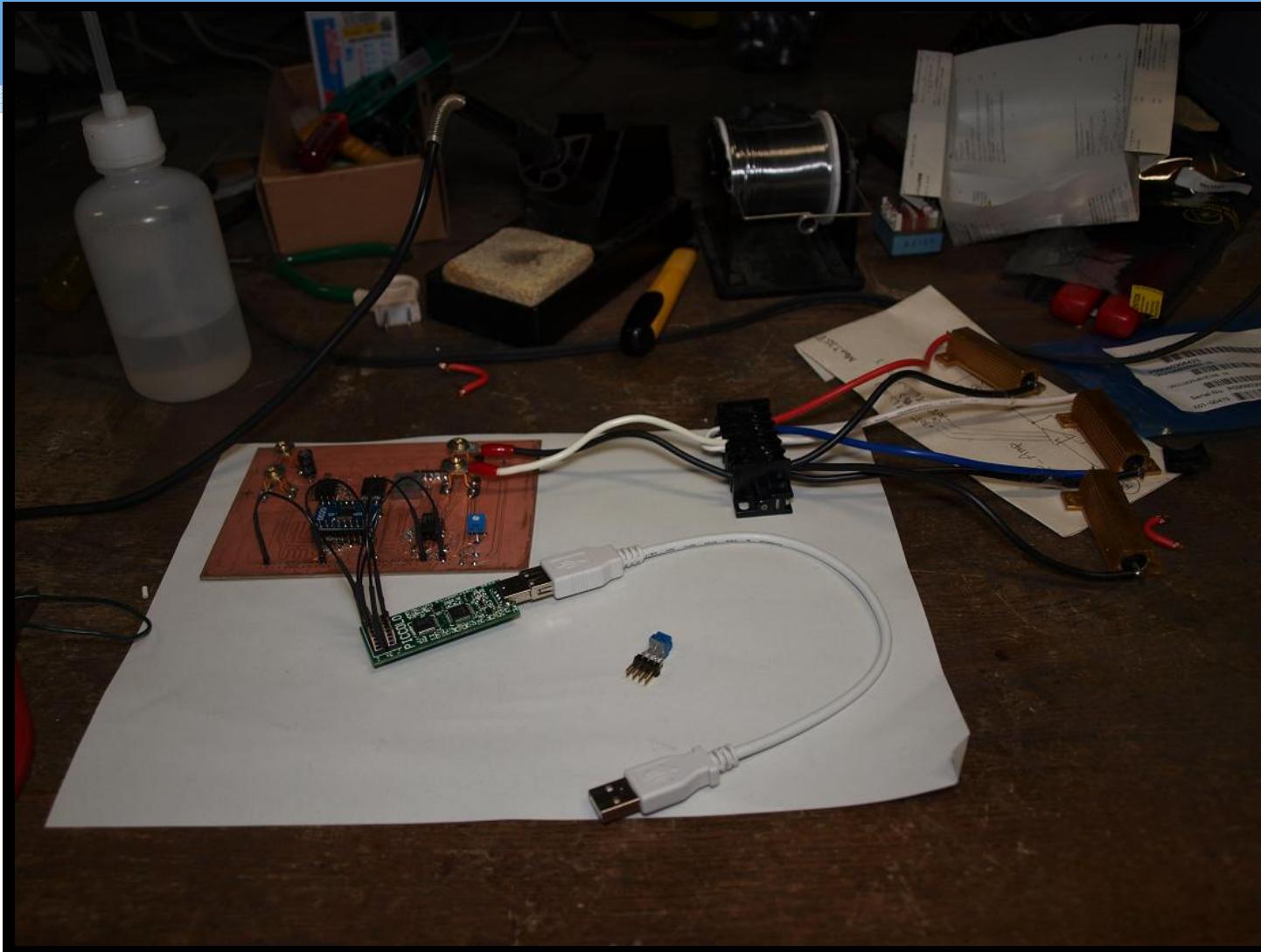
- [1] E. Takegami, K. Higuchi, K. Nakano, S. Tomioka, and K.Watanabe
“The Method for Determining Parameters of Approximate 2DOF Digital Controller for Robust Control of DC-DC Converter” ECTI transactions on electrical eng., electronics, and communications vol.4, no.1 february, 2006.

Training



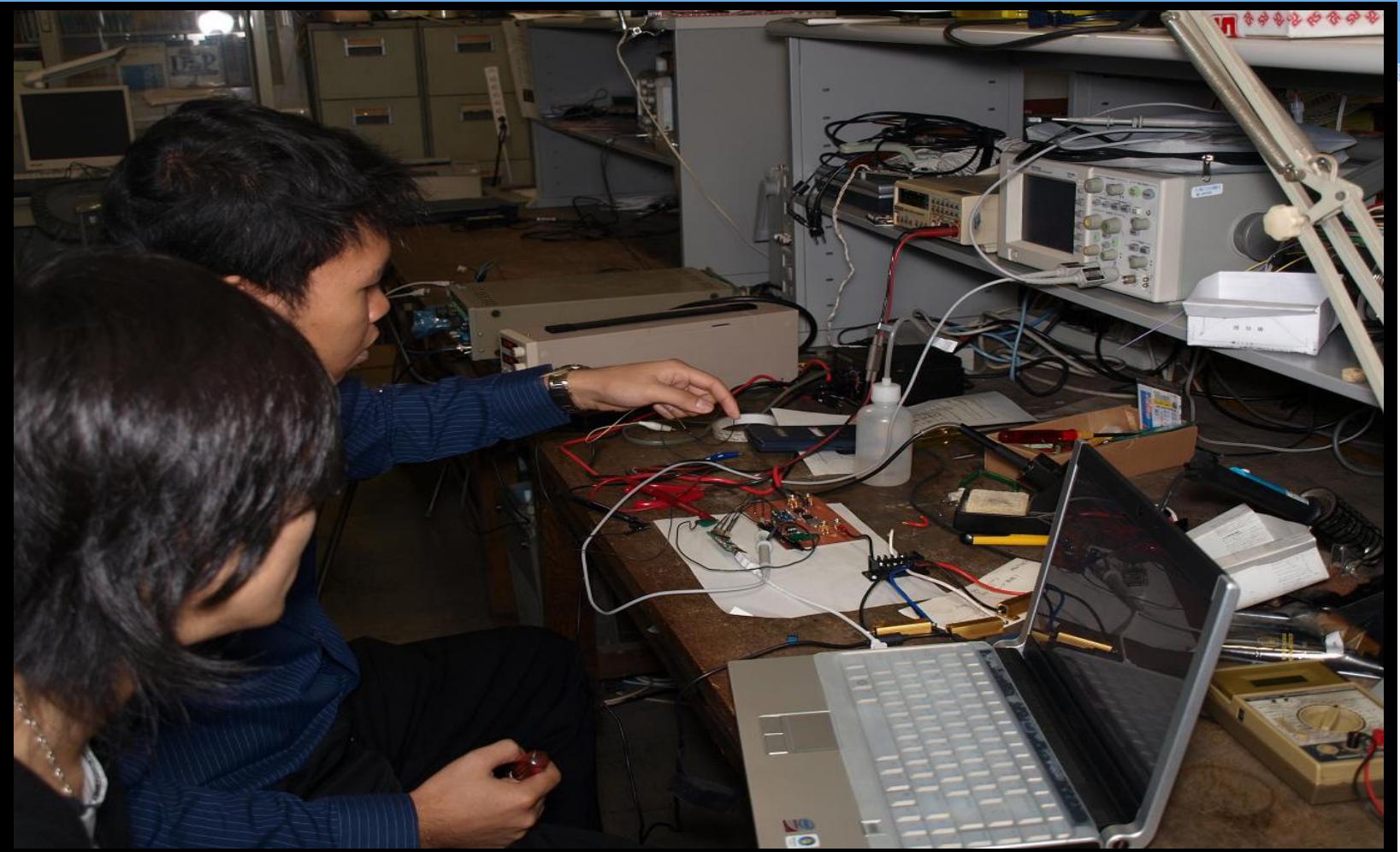
Some prepared PCBs of DC-DC converter

Training



Assembled hardware of DC-DC converter

Training



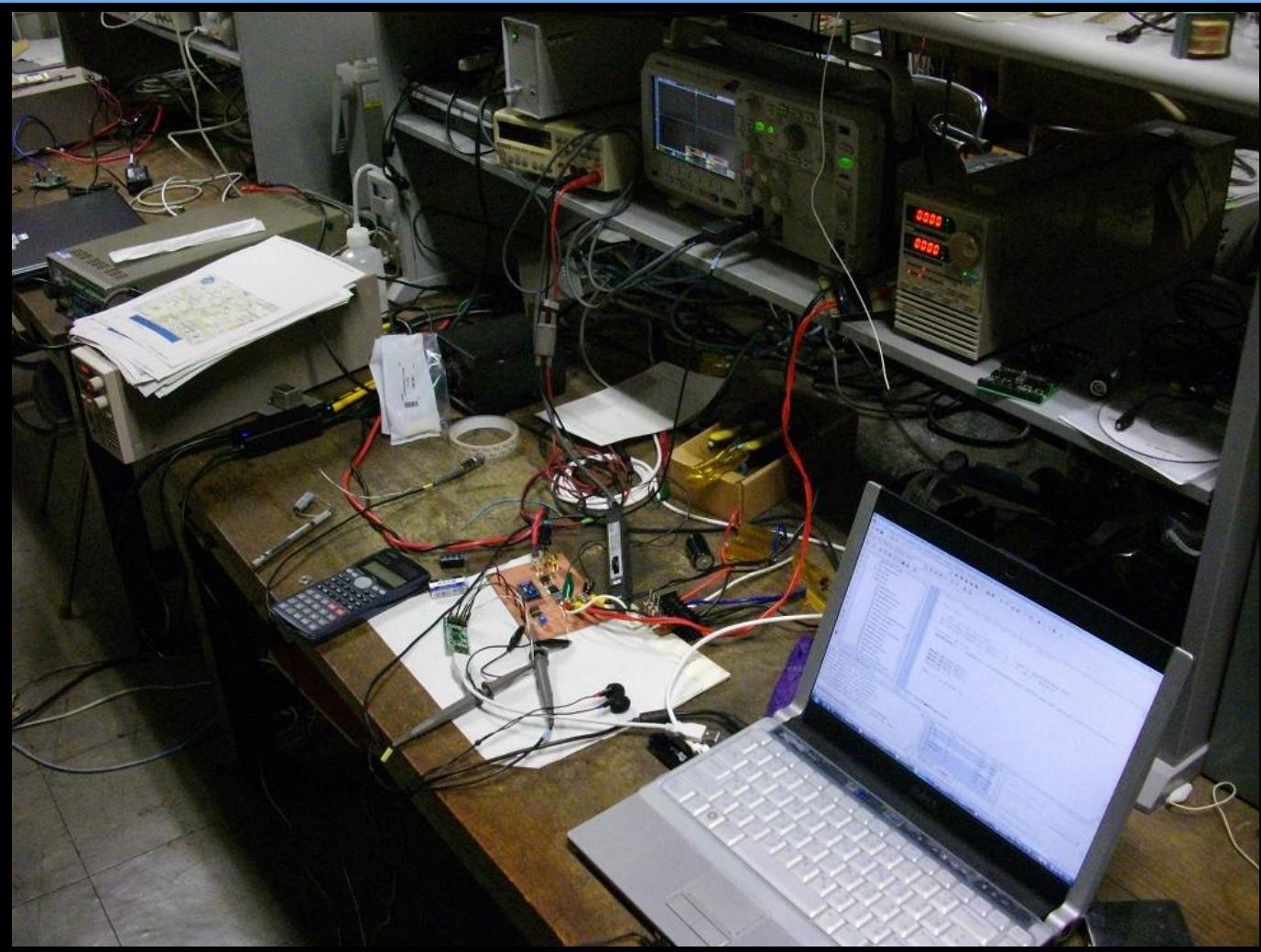
Get training how to program the microcontroller

Training



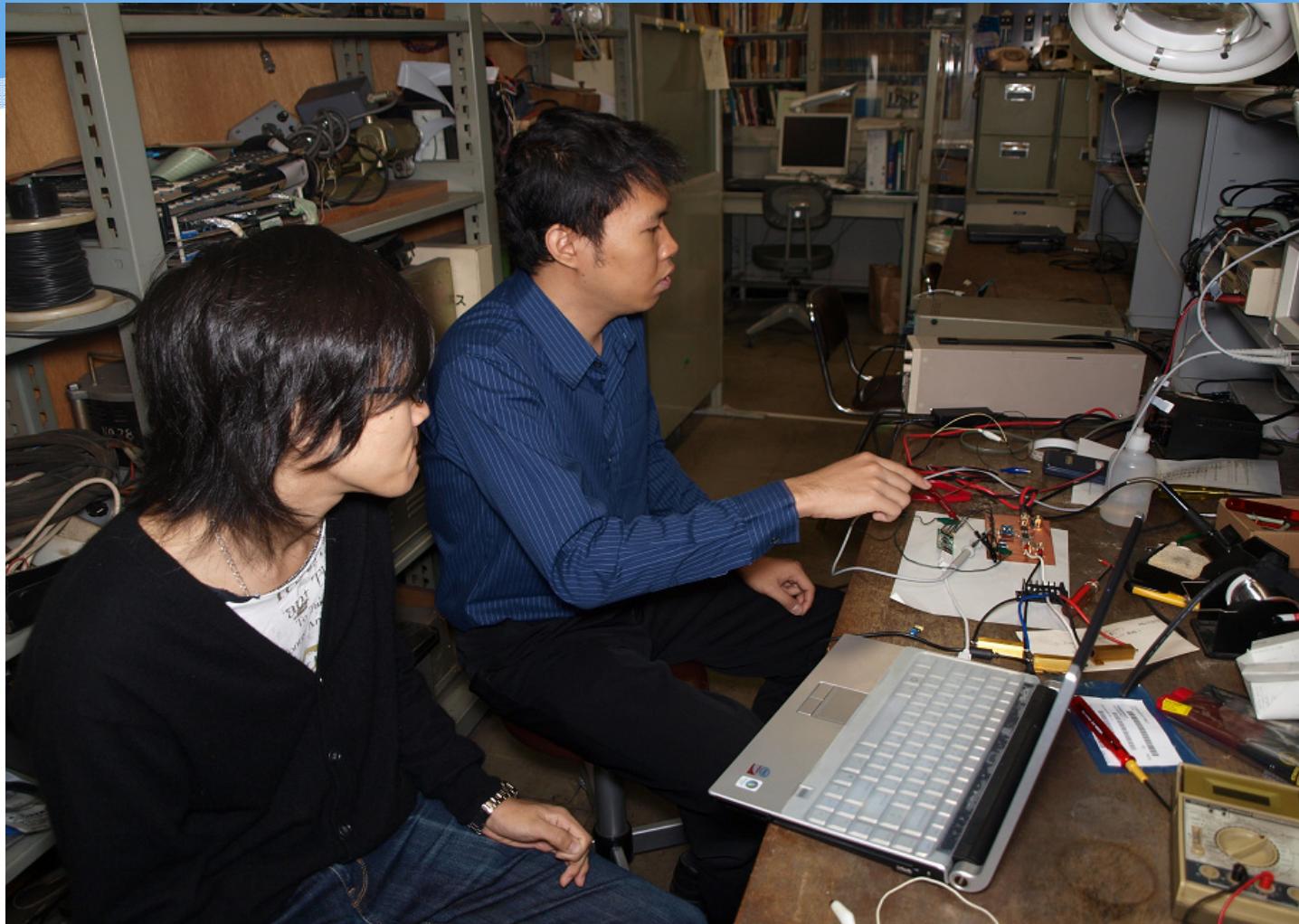
Some control algorithm explanation

Training



Working site

Training



Teeraphat and TA Ohta

Training



Show training to KMUTT, Small screen is KMUTT

Training



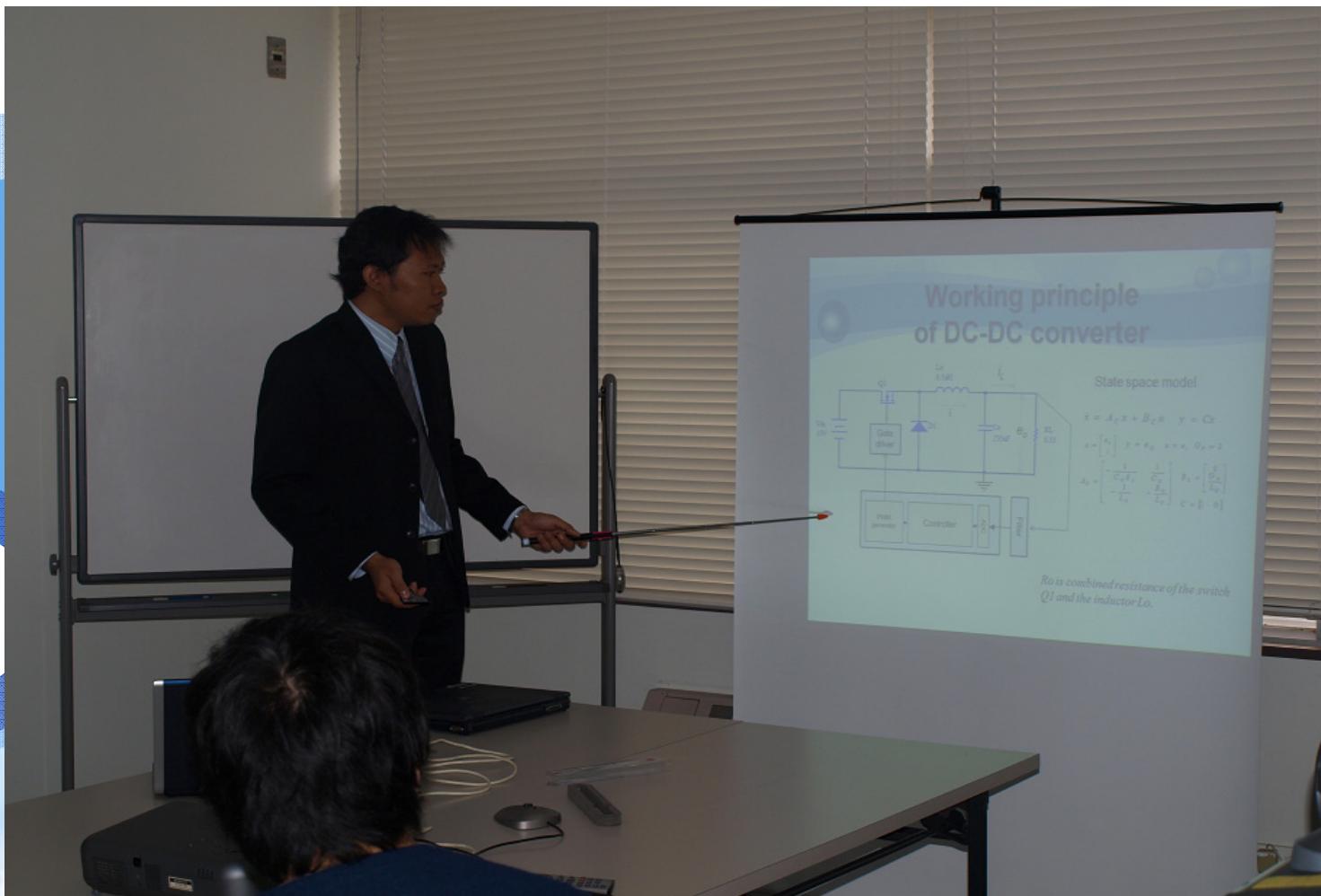
Show training to KMUTT using teleconference
Ohta, Kamon, Higuchi, Teeraphat

Training



Participated in lab meeting

Presentation



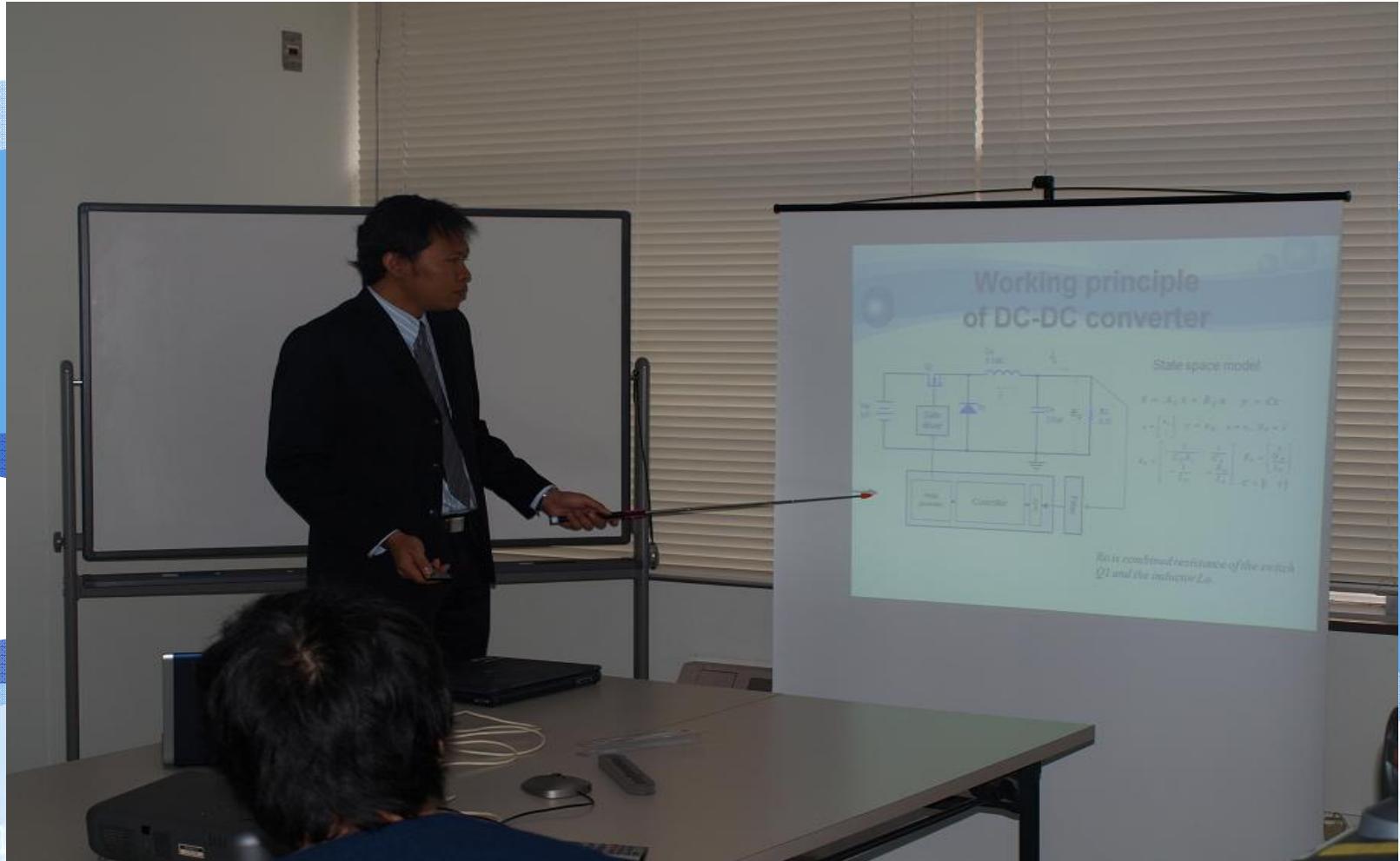
Theeraphat Poomalee

Presentation



Higuchi lab members

Presentation



Theeraphat Poomalee

Presentation



Discussion together with KMUTT

Presentation



Show presentation to KMUTT
Vice President of KMUTT and Prof. Kosin

Presentation



KMUTT students ask questions

Presentation



Prof. Miki ask questions